

Tentative Course Schedule

The material covered this semester mostly will span Chapters 2–7 and part of Chapter 16 in the textbook (Sedra, et al., 8th ed.). Some chapters will be covered thoroughly and some only lightly. Supplemental readings will also be assigned to cover additional topics.

The schedule below should be viewed as a general but tentative framework. Depending on the progression of the course and student interest, some of the material listed below might be omitted, and other material might be added. Specific reading assignments will be posted at the course web site.

Week	Lecture Dates	Lecture Topics
1	Aug. 24–28	Introduction; review of op-amp basics; finite open-loop gain; feedback
2	Aug. 31–Sep. 4	Non-ideal properties of op-amps: input offset voltage and bias currents
3	Sep. 7–11	Op-amp output voltage/current limiting; intro. to diff amps
4	Sep. 14–18	Common-mode rejection ratio; worst-case CMRR; instrumentation amps
5	Sep. 21–25	Op-amp slew rate limiting; intro. to diodes
6	Sep. 28–Oct. 2	Piecewise linear diode models; rectifier circuits; power supply filtering
7	Oct. 5–9	Voltage regulators; load and line regulation
8	Oct. 12–16	Fall Recess; LEDs
9	Oct. 19–23	Intro. to MOSFETs; MOSFET regions of operation
10	Oct. 26–30	MOSFET-based inverting amplifier; MOSFET biasing
11	Nov. 2–6	MOSFET small-signal modeling; practical MOSFET common-source amp
12	Nov. 9–13	MOSFET source follower and common-gate amps; MOSFET output resistance
13	Nov. 16–20	<i>p</i> -channel MOSFETs; CMOS inverter
	Nov. 23–27	Thanksgiving Recess
14	Nov. 30–Dec. 4	Intro. to BJTs (Chap. 6)
15	Dec. 7	“Loose ends” (catch-up)